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**1SCSD** 

# /Mixed-Signal Circuits, is, and Devices (AMS-

**SUBMIT WHITE PAPER** 

## <sup>.</sup> Research

s ng Participation Pledge o Sustainability iidelines Deadlines eria esearch Corp. (SRC) Analog/Mixed-Signal Circuits, Systems, and Devices (AMS-CSD) member eased to solicit white papers for possible funding in 2022.

all domestic and foreign universities and may be addressed by an individual investigator or a ur selection process is divided into two stages. Interested parties are requested to submit a 1-page th should identify what can be done in a three-year period beginning January 1, 2023. A successfully oper will result in an invitation to submit a full proposal. These proposals will be further down research contracts.

ll address existing and emerging challenges in information and communication technologies (ICT) chnologies, as outlined in the Decadal Plan for Semiconductors, and accelerating innovation in

of the members for selected projects to be reviewed and renewed annually, but we anticipate that e a lifetime of 3 years which should help support a mix of research scholars. SRC projects typically research scholars doing a variety of doctoral dissertations, graduate research, undergraduate he post-doctoral work.

ipport each project up to \$90K (USD) per year. The number and size of the contracts awarded will *i* the availability of funds, the support of the research needs, and by the number of high-quality sals offering funding leverage (other funding resources related and beneficial to the proposed aged and details should be described (ex. Fellowships, student support, fabrication support, etc.).

e research selected will be funded through TxACE, directed by Prof. Ken O at the University of

nay be involved in no more than two white papers as either a principal investigator or co-principal

### leeds

ds are outlined in the Research Program needs document. Researchers should carefully review this

## lening Participation Pledge

e next wave of semiconductor innovation and solve the enormous challenges facing our industry, ressive agenda from the Decal Plan for Semiconductors, we must be equally committed to these nts of that success – the people and communities we create and nourish (for example women and ed minorities). Hence, 2030 Broadening Participation Pledge is issued below.

lecade, as SRC defines, selects, and manages its research and education programs, we will look to base, establish a balanced mix of bachelor's, master's, and Ph.D.-level initiatives, and create a more sive community.

## nitment to Sustainability

d growth of semiconductor chip manufacturing in the coming years, it is imperative that the als, and processes involved in their manufacturing are as sustainable as possible. Therefore, ce into consideration the environmental and human health impacts of new chemistries and focus on of more must be environmentally preferable materials and processes chemistries that are more fective, and safer. In general, chemicals that are known to be persistent, bio-accumulative, or toxic more environmentally benign substitutions. Two specific examples include, high global warning jases used for etching and chamber clean and a diverse class of per- and poly-fluoroalkyl substances y as PFAS.

improvements that Moore's law has afforded to semiconductor hardware and the systems they able global appetite for ICT is yielding energy consumption levels that are creating a new headwind advancement of technology. This may limit the growth of our GDP or semiconductors as an a't invest in the discovery of new technologies with radically improved energy efficiency.

## **Pr Guidelines**

limited to 1 page total, using a minimum of 10-point font size, and must be submitted via the application **by Wednesday, June 22, 2022, no later than 11:59 PM EDT.** Submissions not in all guidelines will be excluded from consideration.

#### ne following identifying information in your White Paper:

and university. igator's contact information (telephone number, mailing address, and e-mail address).

#### he following topics in your White Paper:

**!**: Emphasize area and problem to be addressed; match most relevant topic addressed in the document. Projects can cover multiple research needs. Please identify each code covered in the

sent your strategy for addressing the problem. Describe important findings from your research to how your proposed research would advance the state of the art and be useful to SRC member

**Results**: What you plan to accomplish in a 3-year period. What are the anticipated outputs of a rt?

**rticipation:** How your task will enhance diversity in one or more of the following ways. n of SRC's student population

re BS and/or MS students and research/education initiatives

ersity – getting more women, more under-represented minorities (URM) involved - globally • US students into graduate research while still advocating for the best students from across the

#### researchers and young faculty.

**est and participants**: Plan for yearly budget should include overhead charges by your institution. , please also indicate the number of students supported and their degrees pursued. A detailed et is not required at this time.

**age**: Illustrate any leveraged funding which supports the goals and objectives of the proposed ional collaborative funds identified for the project help expand the scope of the proposed research.

: Identify any background intellectual property that either blocks the exercise of license rights or ged by implementation of any of the expected results of this proposed research.

#### es will be expected to:

(s) to work on the project at the start of the contract and encourage them to join the SRC Research n.

SRC's annual student conference, TECHCON, is highly encouraged.

int interactions and hiring by Industry participants.

lls with industry liaisons at least every 4-8 weeks.

cipation in annual project reviews (PI(s) and student(s) are invited).

formance Indicator (KPI) Scorecards yearly associated with annual review.

for pre-defined deliverables in accordance with due dates set in proposal.

tions, posters, thesis, etc. resulting from sponsored research.

lication drafts (conferences, journals, etc.) to SRC at least 60 days prior to anticipated publication

promising areas of research with disclosures sent to SRC.

dget with timely spending and regular invoicing to SRC.

hy events and announcements about you and your team to SRC.

software is to be developed, SRC encourages the use of MIT licensing terms when made available urce.org/licenses/MIT.

xACE activities, which highlight the importance, the need, and progress of research in the analog f the task is placed as part of TxACE).

## Criteria

te papers and later proposals will be accomplished through a technical review of each white paper g the following criteria, which are listed in descending order of relative importance:

ntific and technical merit ovelty, and impact of proposed research Participation GRC's Commitment to Sustainability of proposed investigators, Cost-effectiveness, realism

#### eadlines

	Deadline
of Call-for-White-Papers	Wednesday, June 1, 2022
mit White Papers	Wednesday, June 22, 2022, <b>11:59 PM EDT</b>
omit Full Proposals	Tuesday, August 02, 2022
mit Full Proposals	Monday, August 22, 2022, <b>11:59 PM EDT</b>

als Notified

Friday, September 16, 2022

Start

January 1, 2023

echnical questions to Marcus Pan, Research Program Director. Is and responses should be directed to LaTanya Holmes, Research Program Coordinator.

4819 Emperor Blvd, Suite 300 Durham, NC 27703



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